

Digital to Analog Converters - DAC LC2MOS Dual 12B MDAC Parallel Load Strctr

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	24-SOIC (0.295, 7.50mm Width)
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD7847ARZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The AD7837/AD7847 is a complete, dual, 12-bit multiplying digital-to-analog converter with output amplifiers on a monolithic CMOS chip. No external user trims are required to achieve full specified performance.

Both parts are microprocessor compatible, with high speed data latches and interface logic. The AD7847 accepts 12-bit parallel data which is loaded into the respective DAC latch using the WR input and a separate Chip Select input for each DAC. The AD7837 has a double-buffered 8-bit bus interface structure with data loaded to the respective input latch in two write operations. An asynchronous LDAC signal on the AD7837 updates the DAC latches and analog outputs.

The output amplifiers are capable of developing  $\pm 10$  V across a 2 kOhm load. They are internally compensated with low input offset voltage due to laser trimming at wafer level.

The amplifier feedback resistors are internally connected to VOUT on the AD7847.

The AD7837/AD7847 is fabricated in Linear Compatible CMOS (LC2MOS), an advanced, mixed technology process that combines precision bipolar circuits with low power CMOS logic.

A novel low leakage configuration ensures low offset errors over the specified temperature range.

## Features

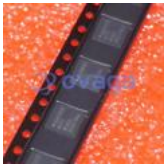
Two 12-Bit MDACs with Output Amplifiers

Space-Saving 0.3", 24-Lead DIP an 24-Terminal SOIC Package

4-Quadrant Multiplication

Parallel Loading Structure: AD7847

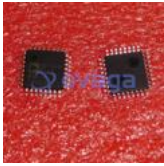
## Related Products



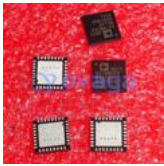
[ADAS3022BCPZ](#)  
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LFCSP-40



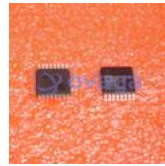
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