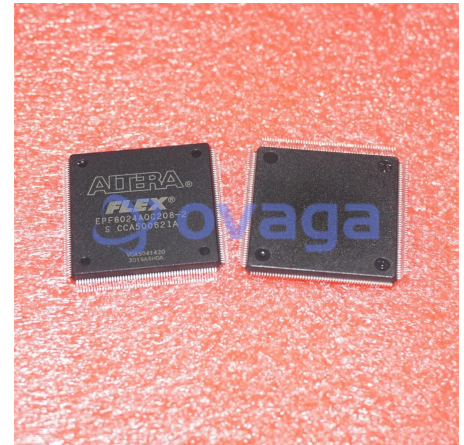


FPGA FLEX 6000 Family 24K Gates 1960 Cells 166.67MHz CMOS Technology 3.3V

Manufacturers	Altera Corporation (Intel)
Package/Case	QFP-208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for EPF6024AQC208-2 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

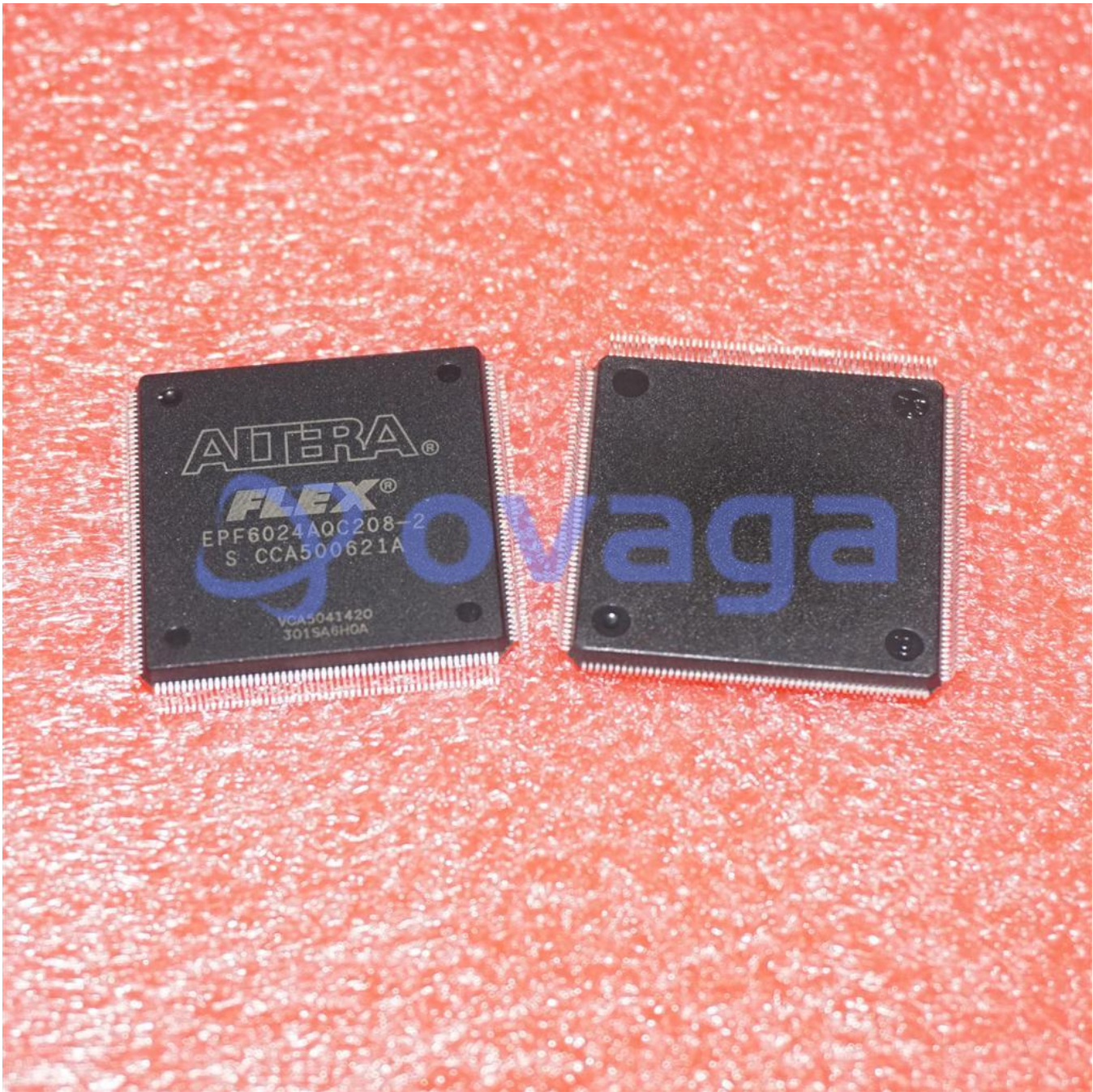
EPF6024AQC208-2 is a complex programmable logic device (CPLD) manufactured by Altera (now part of Intel).

Features

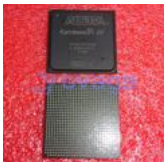
- It has 24 macrocells
- It has a maximum pin count of 208
- It has a maximum user I/O count of 196
- It operates at a maximum frequency of 133 MHz
- It has a programmable power-saving mode
- It supports IEEE 1149.1 boundary-scan testing

Application

- Digital signal processing (DSP)
- High-speed control
- Communication systems
- Industrial automation
- Automotive electronics

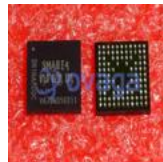


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

Altera Corporation (Intel)
FBGA-256



[EP2C35F672C8N](#)

Altera Corporation (Intel)

FBGA-672



[EPM7128AETC100-10](#)

Altera Corporation (Intel)

TQFP-100



[EP2C35F484C7N](#)

Altera Corporation (Intel)

FBGA-484



[EP2C35F484I8N](#)

Altera Corporation (Intel)

FBGA-484