

# **HMC850LC3**

Data Sheet

<u>RFO</u>

Clock Fanout Buffer 4Out 16Pin SMT

Manufacturers	Analog Devices, Inc	
Package/Case	1 2 Fanout Buffer w	
Product Type	Clock & Timer ICs	
RoHS	Green	
Lifecycle		Images are for reference only

## **General Description**

The HMC850LC3 is a 1:2 Fanout Buffer designed to support data transmission rates up to 28 Gbps, and clock frequencies as high as 20 GHz. All differential inputs and outputs are DC coupled and terminated on chip with 50 Ohm resistors to the positive supply, ground. The outputs may be used in either singleended or differential modes, and should be AC or DC coupled into 50 Ohm resistors connected to ground.

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The HMC850LC3 also features an output level control pin, VR which allows for loss compensation or for signal level optimization. The HMC850LC3 operates from a single -3.3V DC supply and is available in a ceramic RoHS compliant 3x3 mm SMT package.

Please submit RFQ for HMC850LC3 or Email to us: sales@ovaga.com We will contact you in 12 hours.

Features	Application		
Inputs Terminated Internally to 50 Ohms	RF ATE Applications		
Differential Inputs are DC Coupled	Broadband Test & Measurement		
Propagation Delay: 75 ps	Serial Data Transmission up to 28 Gbps		
Fast Rise and Fall Times: 16 / 15 ps	Clock Buffering up to 20 GHz		
Programmable Differential Output Voltage Swing: 600 - 1100 mV			
Power Dissipation: 315 mW			

16 Lead Ceramic 3x3mm SMT Package: 9mm<sup>2</sup>

#### **Related Products**



#### LTC6957HMS-3#PBF

Analog Devices, Inc MSOP-12



## HMC769LP6CE

Analog Devices, Inc 40-QFN



## <u>HMC987LP5E</u>

Analog Devices, Inc 32-VFQFN

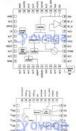
### HMC703LP4E

Analog Devices, Inc QFN-24



# HMC1031MS8E

Analog Devices, Inc 8-MS8E



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### HMC838LP6CE

Analog Devices, Inc QFN-40

#### HMC807LP6CETR

Analog Devices, Inc QFN40

#### HMC835LP6GE

Analog Devices, Inc QFN40