

DSPIC33EP64MC504-I/PT

Data Sheet

Digital Signal Controller, dsPIC33E Series, 140 MHz, 64 KB, 35 I/O's, CAN, I2C, I2S, SPI, UART, USB

Manufacturers	Microchip Technology, Inc	
Package/Case	TQFP-44	Junio and and a second
Product Type	Embedded Processors & Controllers	and a state of the
RoHS	Rohs	
Lifecycle		Images are for reference only
Please submit REO for DSPIC33EP64MC504-I/PT or Email to us; sales@ovaga.com We will contact you in 12 hours REO		

General Description

Microchip's dsPIC33Efamily of digital signal controllers (DSCs) features a 70 MIPS dsPIC® DSC corewith integrated DSP and enhanced onchip peripherals. These DSCs enable thedesign of high-performance, precision motor control systems that are more energyefficient, quieter in operation, have a great range and extended life. They can be used to control brushless DC, permanent magnet synchronous, AC induction andstepper motors. These devices are also ideal for high-performance generalpurpose applications.

Features

Operating Conditions

3.0V to 3.6V, -40°C to +85°C, DC to 70 MIPS

3.0V to 3.6V, -40°C to +150°C, DC to 60 MIPS

dsPIC33E DSC Core

Modified Harvard Architecture

C Compiler Optimized Instruction Set

16-bit Wide Data Path

24-bit Wide Instructions

16x16 Integer Multiply Operations

32/16 and 16/16 Integer Divide Operations

Two 40-bit Accumulators with Rounding and Saturation Options
Single-Cycle Multiply and Accumulate
Single-Cycle shifts for up to 40-bit Data
16x16 Fractional Multiply/Divide Operations
High-Speed PWM
Up to three PWM pairs with independent timing
Dead time for rising and falling edges
7.14 ns PWM resolution
PWM support for Inverters, PFC, Lighting- BLDC, PMSM, ACIM, SRM
Programmable Fault inputs
Flexible trigger configurations for ADC conversions
Advanced Analog Features
ADC module: Configurable as 10-bit, 1.1 Msps with four S&H or 12-bit, 500 ksps with one S&H
Up to three Op amp/Comparators
Op Amp direct connection to the ADC module
Additional dedicated comparator
Programmable references with 32 voltage points for comparators
Charge Time Measurement Unit (CTMU)
Timers/Output Compare/Input Capture
12 general purpose timers
Five 16-bit and up to two 32-bit timers/counters
Four OC modules configurable as timers/counters
PTG module with two configurable timers/counters
32-bit Quadrature Encoder Interface (QEI) module configurable as a timer/counter
Four IC modules
Peripheral Trigger Generator (PTG) for scheduling complex sequences
Communication Interfaces
Two UART modules (15 Mbps)

Two 4-wire SPI modules (15 Mbps)

CANTM module (1 Mbaud) CAN 2.0B support

Two I2CTM modules (up to 1 Mbaud) with SMBus support

PPS to allow function remap

Programmable Cyclic Redundancy Check (CRC)

Direct Memory Access (DMA)

4-channel DMA with user-selectable priority arbitration

UART, SPI, ADC, CAN, IC, OC, and Timers

Related Products



DSPIC30F6014A-20E/PF Microchip Technology, Inc TQFP-80



DSPIC30F5011-30I/PT Microchip Technology, Inc TQFP-64



DSPIC33FJ256MC710-I/PF Microchip Technology, Inc TQFP-100



Microchip Technology, Inc TQFP-64

DSPIC30F5015-30I/PT









DSPIC30F4011-30I/PT

Microchip Technology, Inc

TQFP-100

Microchip Technology, Inc TQFP-44

DSPIC33EP512MU814-I/PH

Microchip Technology, Inc TQFP-144

DSPIC33EP512GM710-I/PF

Microchip Technology, Inc TQFP-100

DSPIC33FJ256GP710-I/PF