EPM7128AETC100-7N

Data Sheet

CPLD MAX 7000A Family 2.5K Gates 128 Macro Cells 129.9MHz 3.3V

Images are for reference only

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for EPM7128AETC100-7N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7128AETC100-7N is a programmable logic device (PLD) manufactured by Intel (formerly Altera), which is a type of digital integrated circuit that can be programmed to perform various logic functions. It is part of the MAX 7000 series of CPLDs (Complex Programmable Logic Devices) and comes in a 100-pin TQFP (Thin Quad Flat Pack) package.

Features

128 macrocells with 36 inputs each

128 flip-flops

5,000 usable gates

8 input pins and 34 I/O pins

Programmable security bit to prevent unauthorized access

5V tolerant I/O pins

7.5 ns maximum pin-to-pin delay

Application

Embedded control systems

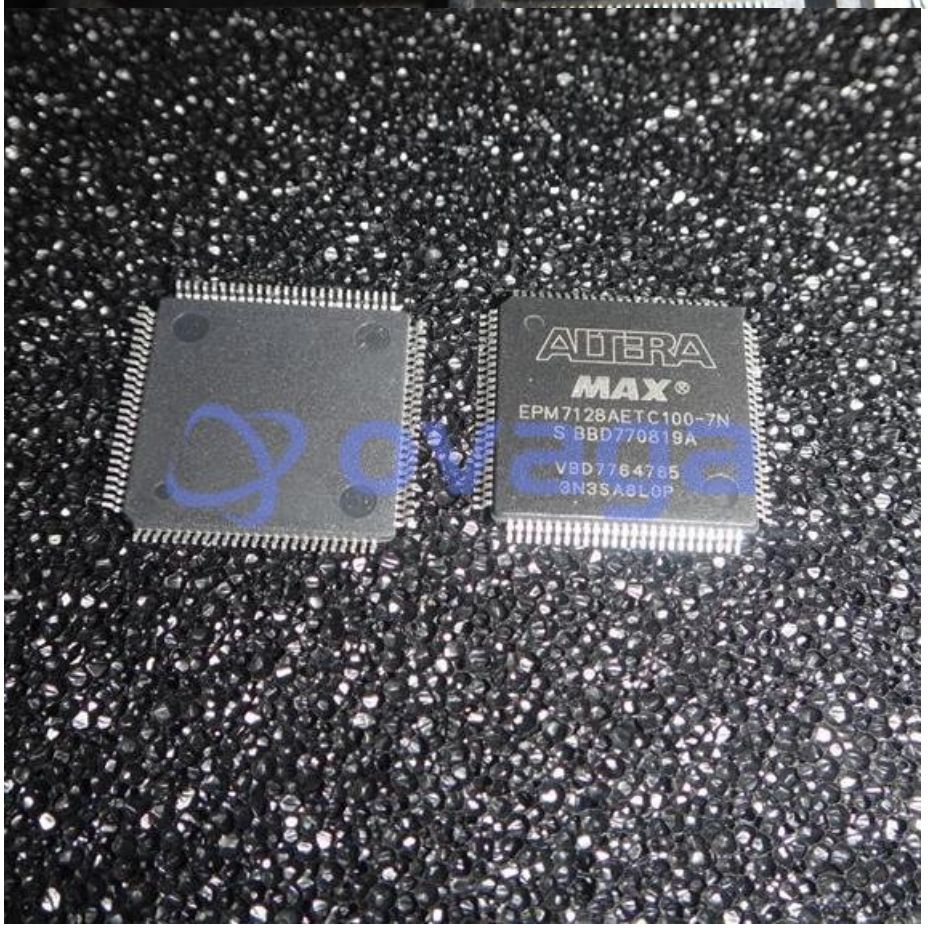
Digital signal processing

System level integration

Communications and networking equipment

Industrial automation

Automotive electronics



Related Products

[EP4CE55F29C8N]

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N]

Altera Corporation (Intel)

BGA-100

[EPM1270T144A5N]

Altera Corporation (Intel)

TQFP-144

[EPM570F256C5N]

Altera Corporation (Intel)

FBGA-256

[EP2C35F672C8N]

Altera Corporation (Intel)

FBGA-672

[EPM7128AETC100-10]

Altera Corporation (Intel)

TQFP-100

[EP2C35F484C7N]

Altera Corporation (Intel)

FBGA-484

[EP2C35F484I8N]

Altera Corporation (Intel)

FBGA-484