

ADP5061ACBZ-5-R7

Data Sheet

<u>RFO</u>

Linear Battery Charger Li-Ion 2100mA 20-Pin WLCSP T/R

Manufacturers	Analog Devices, Inc	
Package/Case	20-WFBGA, WLCSP	0000000
Product Type	Power Management ICs	0000
RoHS	Pb-free Halide free	
Lifecycle		Images are for reference only

General Description

The ADP5061 charger is fully compliant with USB 3.0 and the USB Battery Charging Specification 1.2 and enables charging via the mini USB VBUS pin from a wall charger, car charger, or USB host port.

Please submit RFQ for ADP5061ACBZ-5-R7 or Email to us: sales@oyaga.com We will contact you in 12 hours.

The ADP5061 operates from a 4 V to 6.7 V input voltage range but is tolerant of voltages up to 20 V. The 20 V voltage tolerance alleviates the concerns about the USB bus spiking during disconnect or connect scenarios.

The ADP5061 features an internal FET between the linear charger output and the battery. This permits battery isolation and, hence, system powering under a dead battery or no batteryscenario, which allows for immediate system function on connection to a USB power supply.

Based on the type of USB source, which is detected by an external USB detection chip, the ADP5061 can be set to apply the correct current limit for optimal charging and USB compliance.

The ADP5061 has three factory programmable digital input/output pins that provide maximum flexibility for different systems. These digital input/output pins permit combinations of features such as, input current limits, charging enable and disable, charging current limits, and a dedicated interrupt output pin.

Features

2.6 mm x 2 mm WLCSP Package

Fully programmable via I2C

Flexible digital control inputs

Up to 2.1 A current from an ac charger in LDO mode

Operating input voltage from 4.0 V to 6.7 V

Tolerant input voltage -0.5 V to +20 V (USB VBUS)

Fully compatible with USB 3.0 and USB Battery Charging Specification 1.2

Built-in current sensing and limiting

As low as 30 m $\!\Omega$ battery isolation FET between battery and charger output

Thermal regulation prevents over heating

Compliant with JEITA 1 and JEITA 2 Li-Ion battery charging temperature specifications

SYS_EN flag permits the system to be disabled until battery is at minimum required level for guaranteed system start-up.

Related Products



ADP3336ARMZ-REEL7 Analog Devices, Inc



ADP3367ARZ Analog Devices, Inc SOIC-8

MSOP-8



ADP3330ARTZ3.3-RL7 Analog Devices, Inc



ADR421ARZ Analog Devices, Inc SOP-8

SOT-23-6



<u>AD737JRZ</u>

Analog Devices, Inc SOP-8

<u>AD636JH</u>

Analog Devices, Inc TO-100-10

ADR434BRZ

Analog Devices, Inc SOIC-8

ADR3412ARJZ-R7 Analog Devices, Inc

SOT-23-6

equipment

Single cell Li-Ion portable

Application

Digital still cameras

Digital video cameras

PDAs, audio, GPS devices

Portable medical devices

Mobile phones