LTC2291IUP

Data Sheet

Dual 12-Bit, 65/40/25Msps Low Power 3V ADCs

Manufacturers	Analog Devices, Inc
Package/Case	QFN-64
Product Type	Data Conversion ICs
RoHS	
Lifecycle	

Images are for reference only

Please submit RFQ for LTC2291IUP or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

LTC2291IUP is a high-speed, 16-bit analog-to-digital converter (ADC) manufactured by Linear Technology (now part of Analog Devices). It is a member of the LTC2291 family of ADCs and features a sampling rate of up to 250 MSPS (mega samples per second) with a power consumption of 1.05 watts.

Features

16-bit resolution with no missing codes

High speed: up to 250 MSPS sampling rate

Low power consumption: 1.05 watts

SNR (signal-to-noise ratio) of 74.5 dBFS (decibels relative to full scale)

SFDR (spurious-free dynamic range) of 91 dBc (decibels relative to the carrier)

Application

Communications systems

Instrumentation

Digital signal processing (DSP)

Radar systems

Medical imaging

Related Products

[LTC1860IMS8#PBF](#)

Analog Devices, Inc

MSOP-8

[LTC2351IUH-14#PBF](#)

Analog Devices, Inc

QFN-32

[LT1171CQ](#)

Analog Devices, Inc

TO-263

[LTC2600CGN#PBF](#)

Analog Devices, Inc

SSOP16

[LTC2485IDD#PBF]

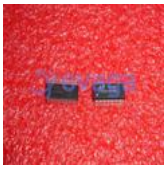
Analog Devices, Inc

DFN-10

[LTC2642CMS-16#PBF]

Analog Devices, Inc

10MSOP



[LTC2418IGN#PBF]

Analog Devices, Inc

SSOP28

[LTC1865AIMS#PBF]

Analog Devices, Inc

MSOP-1