

Low Skew 2 Input MUX And 1 to 8 Clock Buffer; HT SUSA CODE:8542390000

Manufacturers	Renesas Technology Corp
Package/Case	TSSOP-16
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 552G-02ILN or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 552-02 is a low skew, single-input to eightoutput clock buffer. The device offers a dual input with pin select for switching between two clock sources. It is part of IDT's ClockBlocks™ family. See the 553 for a 1 to 4 low skew buffer. For more than 8 outputs see the MK74CBxxx Buffalo™ series of clock drivers. IDT makes many non-PLL and PLL based low skew output devices as well as Zero Delay Buffers to synchronize clocks. Contact us for all of your clocking needs.

Features

Extremely low skew outputs (50ps maximum)

Packaged in 16 pin TSSOP

Pb (lead) free package

Low power CMOS technology

Operating Voltages of 2.5 V to 5 V

Output Enable pin tri-states outputs

5 V tolerant input clocks

Input/Output clock frequency up to 200 MHz

Input clock multiplexer simplifies clock selection

Industrial temperature

Related Products



[ICM7555IBAZ-T](#)

Renesas Technology Corp
SOIC-8



[557MI-01LF](#)

Renesas Technology Corp
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[553MILFT](#)

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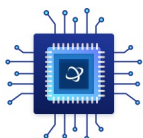
[553SDCGI8](#)

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[553SCMG18](#)

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COL-8



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