

SRAM Chip Async Single 5V 64K-Bit 8K x 8 150ns

Manufacturers [Renesas Technology Corp](#)

Package/Case CDIP-28

Product Type Memory

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for HM1-65642/883 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The HM-65642/883 is a CMOS 8192 x 8-bit Static Random Access Memory. The pinout is the JEDEC 28 pin, 8-bit wide standard, which allows easy memory board layouts which accommodate a variety of industry standard ROM, PROM, EPROM, EEPROM and RAMs. The HM-65642/883 is ideally suited for use in microprocessor based systems. In particular, interfacing with the Intersil 80C86 and 80C88 microprocessors is simplified by the convenient output enable(G) input. The HM-65642/883 is a full CMOS RAM which utilizes an array of six transistor (6T) memory cells for the most stable and lowest possible standby supply current over the full military temperature range.

Features

This Circuit is Processed in Accordance to MIL-STD- 883 and is Fully Conformant Under the Provisions of Paragraph 1.2.1.

Full CMOS Design

Six Transistor Memory Cell

Low Standby Supply Current 100 μ A

Low Operating Supply Current 20mA

Fast Address Access Time 150ns

Low Data Retention Supply Voltage 2.0V

CMOS/TTL Compatible Inputs/Outputs

JEDEC Approved Pinout

Equal Cycle and Access Times

No Clocks or Strobes Required

Gated Inputs

No Pull-Up or Pull-Down Resistors Required

Temperature Range -55°C to +125°C

Easy Microprocessor Interfacing

Dual Chip Enable Control



Related Products



[HM1-65162C/883](#)

Renesas Technology Corp
CDIP-24



[HM1-65162/883](#)

Renesas Technology Corp
CDIP24



[HM1-6617/883](#)

Renesas Technology Corp
SBCDIP -24



[HM28100TTI5SE](#)

Renesas Technology Corp
44-TSOP (0.400, 10.16mm Width)



[HM1-6642B/883](#)

Renesas Technology Corp
CDIP-24



[HM1-65642B/883](#)

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CDIP28



[HM1-6551B/883](#)

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CDIP-22



[HM1-6514/883](#)

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CDIP-18