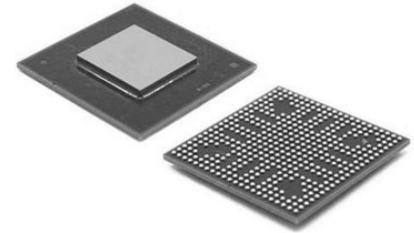


3 A, 1.2 MHz/600 kHz High Efficiency Synchronous Step-Down DC-to-DC Regulator;  
 Package: 16-LFCSP (Leadform Chip Scale); Temperature Range: -40°C to +125°C

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	LFCSP-16
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADP2118ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADP2118 is a low quiescent current, synchronous, step-down dc-to-dc regulator in a compact 4mm × 4mm LFCSP package. It uses a current mode, constant frequency pulse-width modulation (PWM) control scheme for excellent stability and transient response. Under light loads, the ADP2118 can be configured to operate in pulse frequency modulation (PFM) mode that reduces switching frequency to save power.

The ADP2118 runs from input voltages of 2.3 V to 5.5 V. The output voltage of the ADP2118ACPZ-R7 is adjustable from 0.6 V to input voltage (VIN), and the ADP2118ACPZ-x.x-R7 are available in preset output voltage options of 1.2 V and 3.3 V. The ADP2118 requires minimal external parts and provides a high efficiency solution with its integrated power switch, synchronous rectifier, and internal compensation. The IC draws less than 3 μA from the input source when it is disabled. Other key features include undervoltage lockout (UVLO), integrated soft start to limit inrush current at startup, overvoltage protection (OVP), overcurrent protection (OCP), and thermal shutdown (TSD).

## Features

3 A continuous output current

75 m $\Omega$  and 40 m $\Omega$  integrated FET

Input voltage range from 2.3 V to 5.5 V

Output voltage from 0.6 V to VIN

600 kHz or 1.2 MHz fixed switching frequency

Synchronizable between 600 kHz and 1.4 MHz

Selectable synchronize phase shift: 0° or 180°

Selectable PWM or PFM mode operation

Current mode architecture

Precision enable input

Power good output

Voltage tracking input

Integrated soft start

Internal compensation

Starts up into a precharged output

UVLO, OVP, OCP, and thermal shutdown

Available in 16-lead, 4 mm  $\times$  4 mm LFCSP package

Supported by ADIsimPower design tool

## Application

Point of load conversion

Communications and networking equipments

Industrial and instrumentation

Consumer electronics

Medical appliances

## Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc  
MSOP-8



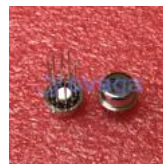
[AD737JRZ](#)

Analog Devices, Inc  
SOP-8



[ADP3367ARZ](#)

Analog Devices, Inc  
SOIC-8



[AD636JH](#)

Analog Devices, Inc  
TO-100-10



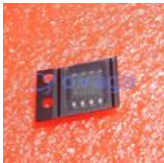
[ADP3330ARTZ3.3-RL7](#)

Analog Devices, Inc  
SOT-23-6



[ADR434BRZ](#)

Analog Devices, Inc  
SOIC-8



[ADR421ARZ](#)

Analog Devices, Inc  
SOP-8



[ADR3412ARJZ-R7](#)

Analog Devices, Inc  
SOT-23-6