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AD9764ARU

(MSB) DB13 1

Data Sheet

Digital to Analog Converters - DAC 14-Bit 100 MSPS A/D Converter

| | | DB12 2 27 DVDD |
|---------------|-----------------------|--|
| Monutostumono | Analog Dorrigos, Inc. | DB11 3 26 DCOM |
| Manufacturers | Analog Devices, Inc | DB10 4 25 NC |
| | | |
| | | DB8 6 TOP VIEW 23 COMP2 DB7 7 (Not to Scale) 22 Joura |
| Package/Case | TSSOP-28 | DB6 8 21 IOUTB |
| | | DB6 8 21 IoUTB DB5 9 20 ACOM |
| | | DB4 10 19 COMP1 |
| Product Type | Data Conversion ICs | DB3 11 18 FS ADJ |
| | | DB2 12 17 REFIO DB1 13 16 REFLO |
| | | DB0 14 15 SLEEP |
| RoHS | | |
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| Lifeerale | | Images are for reference only |
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Please submit RFQ for AD9764ARU or Email to us: sales@ovaga.com We will contact you in 12 hours.

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General Description

The AD9764 is the 14-bit resolution member of the TxDAC®series of high performance, low power CMOS digital-to-analogconverters (DACs). The TxDAC®; family, which consists of pincompatible 8-, 10-, 12-, and 14-bit DACs, is specificallyoptimized for the transmit signal path of communicationsystems. All of the devices share the same interface options, small outline package and pinout, providing an upward ordownward component selection path based on performance, resolution and cost. The AD9764 offers exceptional ac and deperformance while supporting update rates up to 125 MSPS.

The AD9764's flexible single-supply operating range of 2.7 V to 5.5 V and low power dissipation are well suited for portable and/ow power applications. Its power dissipation can be further educed to a mere 45 mW with a slight degradation in performance by lowering the full-scale current output. Also, a power-downmode reduces the standby power dissipation to approximately 25 mW.

The AD9764 is manufactured on an advanced CMOS process. A segmented current source architecture is combined with aproprietary switching technique to reduce spurious components and enhance dynamic performance. Edge-triggered inputlatches and a 1.2 V temperature compensated bandgap referencehave been integrated to provide a complete monolithicDAC solution. Flexible supply options support +3 V and +5 VCMOS logic families.

The AD9764 is a current-output DAC with a nominal full-scaleoutput current of 20 mA and >100 k Ω output impedance. Differential current outputs are provided to support single-endedor differential applications. Matching between the twocurrent outputs ensures enhanced dynamic performance in adifferential output configuration. The current outputs may betied directly to an output resistor to provide two complementary, single-ended voltage outputs or fed directly into a transformer. The output voltage compliance range is 1.25 V.

The on-chip reference and control amplifier are configured formaximum accuracy and flexibility. The AD9764 can be driven by the on-chip reference or by a variety of external reference voltages. The internal control amplifier, which provides a wide(>10:1) adjustment span, allows the AD9764 full-scale current be adjusted over a 2 mA to 20 mA range while maintainingexcellent dynamic performance. Thus, the AD9764 may operate reduced power levels or be adjusted over a 20 dB range toprovide additional gain ranging capabilities.

The AD9764 is available in a 28-lead SOIC package. It isspecified for operation over the industrial temperature range.

Product Highlights

The AD9764 is a member of the TxDAC product family that provides an upward or downward component selection pathbased on resolution (8 to 14 bits) performance and cost

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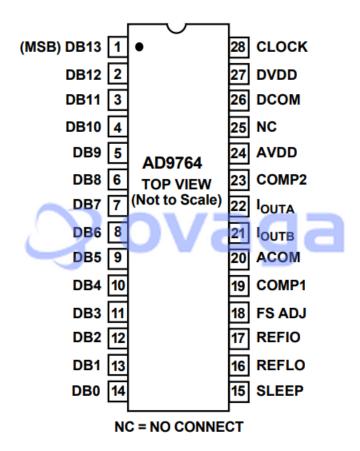
Manufactured on a CMOS process, the AD9764 uses a proprietaryswitching technique that enhances dynamic performancebeyond that previously attainable by higher power/costbipolar or BiCMOS devices.

On-chip, edge-triggered input CMOS latches readily interfaceto +3 V and +5 V CMOS logic families. The AD9764 cansupport update rates up to 125 MSPS.

A flexible single-supply operating range of 2.7 V to 5.5 V, and a wide full-scale current adjustment span of 2 mA to 20 mA, allows the AD9764 to operate at reduced power levels.

The current output(s) of the AD9764 can be easily configured for various single-ended or differential circuit topologies.

| Features | Application | | |
|--|---|--|--|
| Member of pin-compatible TxDAC product family | Communication Transmit Channel:- Basestations- ADSL/HFC Moderns | | |
| 125 MSPS update rate | Instrumentation | | |
| 14-bit resolution | | | |
| Excellent SFDR and IMD | | | |
| Differential current outputs: 2 mA to 20 mA | | | |
| Power dissipation: 190 mW at 5 V to 45 mW at 3 V | | | |
| Power-down mode: 25 mW at 5 V | | | |
| On-chip 1.20 V reference | | | |
| Single +5 V or +3 V supply operation | | | |
| Packages: 28-lead SOIC and TSSOP | | | |
| Edge-triggered latches | | | |
| | | | |



Related Products



ADAS3022BCPZ Analog Devices, Inc



AD574AJNZ

LFCSP-40

Analog Devices, Inc PDIP-28



Analog Devices, Inc TQFP-32

AD7938BSUZ



AD7124-8BCPZ-RL7 Analog Devices, Inc LFCSP-32









TSSOP-24

Analog Devices, Inc

AD9680BCPZ-500

Analog Devices, Inc LFCSP-64

AD7266BSUZ

Analog Devices, Inc TQPF-32

AD7401YRWZ

Analog Devices, Inc SOIC-16

AD7192BRUZ-REEL