

256 Macrocell CPLD

Manufacturers

[AMD Xilinx, Inc](#)

Package/Case

PQFP208

Product Type

Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XCR3256XL-12PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

## General Description

XCR3256XL-12PQ208C is a programmable logic device (PLD) from the Xilinx XC9500XL family.

## Features

It has 256 macrocells with 5V tolerant inputs.

It has a maximum propagation delay of 12 ns and a maximum frequency of 83.3 MHz.

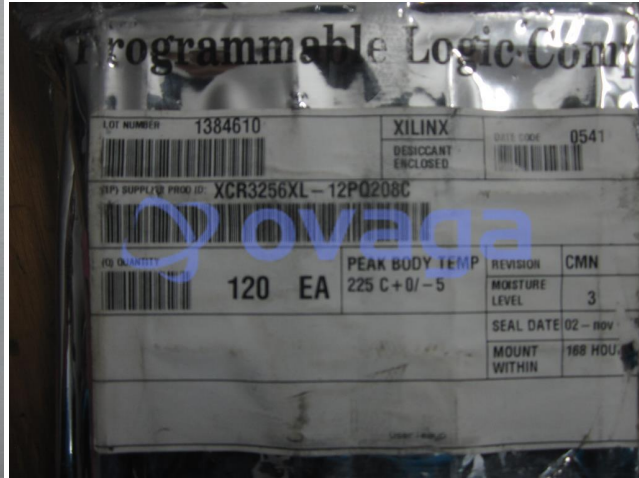
It operates at a voltage range of 3.0V to 3.6V.

It has 208 pins in a PQFP package.

## Application

It is commonly used in digital circuits such as control systems, digital signal processing, and communication systems.

It is also used in automotive, industrial and medical applications.

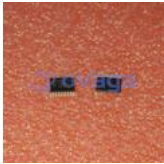


### Related Products



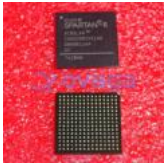
#### [XC18V01S020C](#)

AMD Xilinx, Inc  
SOP-20



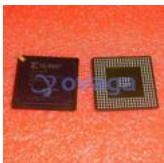
#### [XCF04SV0G20C](#)

AMD Xilinx, Inc  
TSSOP20



#### [XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc  
BGA-225



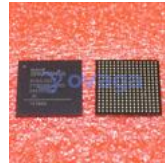
#### [XCV50-6BG256C](#)

AMD Xilinx, Inc  
BGA256



#### [XCF08PV0G48C](#)

AMD Xilinx, Inc  
TSOP-48



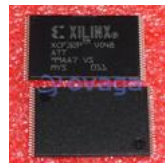
#### [XC6SLX25-3FTG256C](#)

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BGA-256



#### [XC6SLX16-3CSG324C](#)

AMD Xilinx, Inc  
BGA-324



#### [XCF32PVO48C](#)

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TSOP48