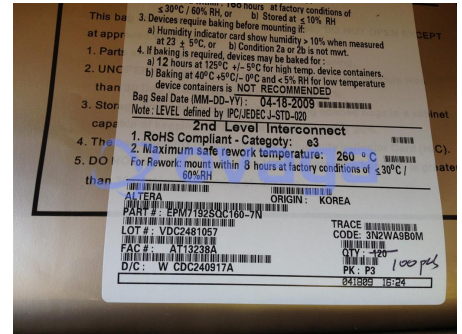


CPLD. MAX 7000, 192 MACROCELLS, PQFP160; Logic IC family:CPLD (EPLD); Logic IC Base Number:7192; Logic IC function:EPM7192S; Voltage, supply:5V; Case style:PQFP; Gates, No. of:3750; I/O lines, No. of:124; Macrocells, No. of:192; RoHS Compliant: Yes

Manufacturers	Altera Corporation (Intel)
Package/Case	PQFP-160
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for EPM7192SQC160-7N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM7192SQC160-7N is a specific model number of a field-programmable gate array (FPGA) manufactured by Intel (formerly Altera). It is part of the MAX 7000S series of FPGAs, which are known for their high performance and versatility.

Features

It has 7192 logic elements (LEs) which are programmable building blocks used to implement digital logic circuits.

It has 224 input/output (I/O) pins, which are used for interfacing with external devices.

It operates at a maximum frequency of 125 MHz, making it suitable for a wide range of applications.

It has 256 kilobits (Kb) of embedded memory for storing data.

It supports various I/O standards such as LVTTTL, LVCMOS, and PCI.

Application

EPM7192SQC160-7N FPGA can be used in various applications including digital signal processing (DSP), telecommunications, industrial control, embedded systems, and automotive electronics.

It can be used in high-speed data communication systems, where it can implement protocols, perform data encryption/decryption, and handle data routing.

It can be used in motor control systems, where it can implement motor control algorithms, handle sensor data, and provide precise timing signals.

It can be used in video processing applications, where it can implement image and video processing algorithms, handle video interfaces, and perform real-time video analytics.

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100 hours at factory conditions of
≤ 30°C / 60% RH, or b) Stored at ≤ 10% RH
3. Devices require baking before mounting if:
a) Humidity indicator card show humidity > 10% when measured
at 23 ± 5°C, or b) Condition 2a or 2b is not mwmt.
4. If baking is required, devices may be baked for :
a) 12 hours at 125°C +/- 5°C for high temp. device containers.
b) Baking at 40°C +5°C/- 0°C and < 5% RH for low temperature
device containers is **NOT RECOMMENDED**

NOT OPEN RECEPT

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Bag Seal Date (MM-DD-YY) : 04-18-2009
Note : LEVEL defined by IPC/JEDEC J-STD-020

2nd Level Interconnect

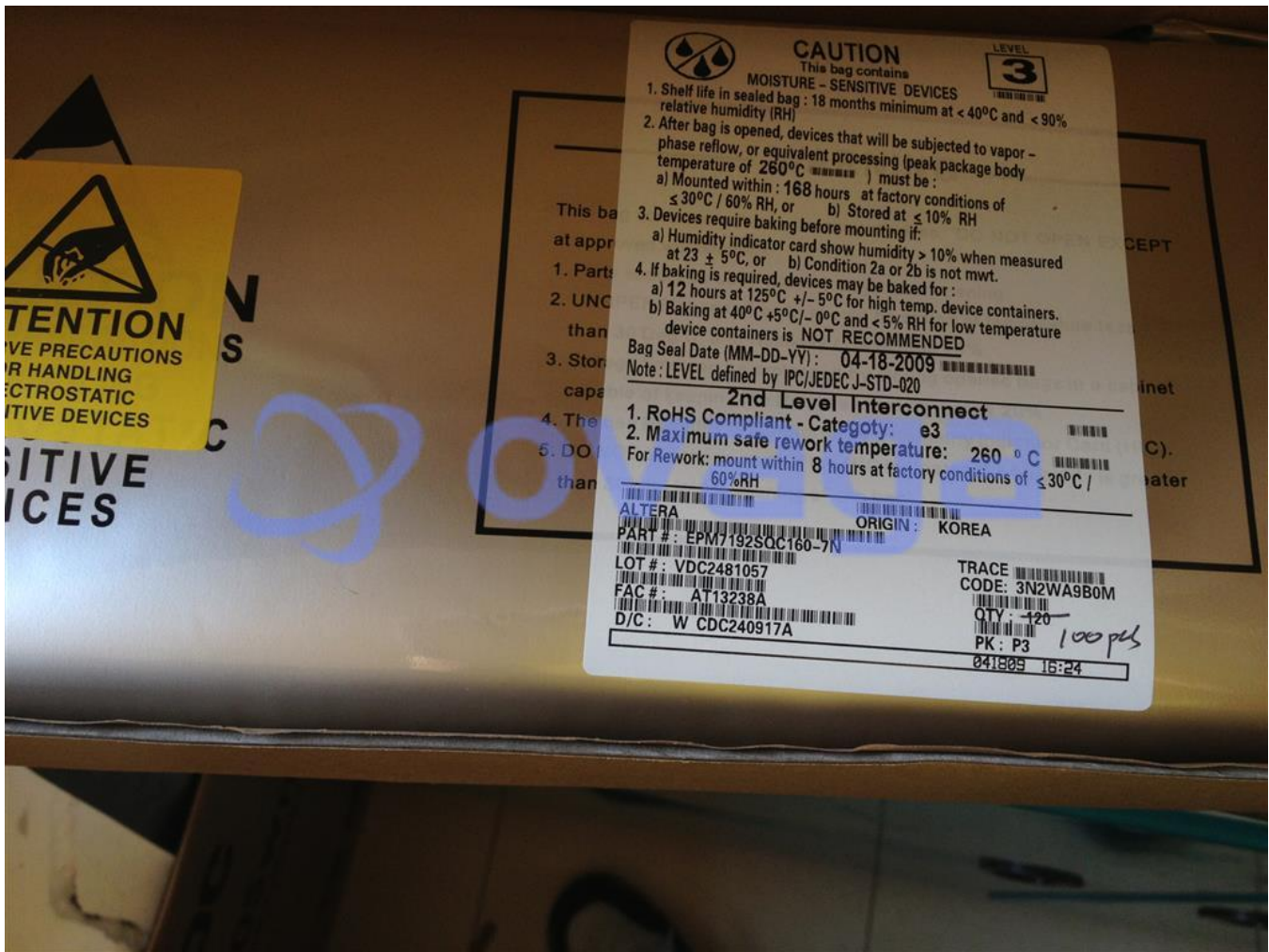
- 1. RoHS Compliant - Category: e3
 - 2. Maximum safe rework temperature: 260 °C
- For Rework: mount within 8 hours at factory conditions of ≤ 30°C / 60%RH

ALTERA
ORIGIN : KOREA
PART # : EPM7192SQC160-7N
LOT # : VDC2481057
FAC # : AT13238A
D/C : W CDC240917A

TRACE
CODE: 3N2WA9B0M
QTY : 120
PK : P3

100 pks

041809 16:24

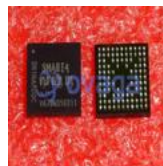


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

Altera Corporation (Intel)
FBGA-256



[EP2C35F672C8N](#)

Altera Corporation (Intel)
FBGA-672



[EPM7128AETC100-10](#)

Altera Corporation (Intel)
TQFP-100



[EP2C35F484C7N](#)

Altera Corporation (Intel)
FBGA-484



[EP2C35F484I8N](#)

Altera Corporation (Intel)
FBGA-484