

Differential Line Transceiver RS422/RS485 Driver, 4.75V-5.25V supply, NSOIC-8

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-8
Product Type	Interface ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADM4855ARZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADM4850 / ADM4851 / ADM4852 / ADM4853 / ADM4854 / ADM4855 / ADM4856 / ADM4857 are differential line transceivers suitable for high speed, half-duplex and full duplex data communication on multipoint bus transmission lines. They are designed for balanced data transmission and comply with EIA Standards RS-485 and RS-422. The ADM4850 / ADM4851 / ADM4852 / ADM4853 are half-duplex transceivers that share differential lines and have separate enable inputs for the driver and receiver. The full duplex ADM4854 / ADM4855 / ADM4856 / ADM4857 transceivers have dedicated differential line driver outputs and receiver inputs.

The devices have a 1/8-unit load receiver input impedance, which allows up to 256 transceivers on one bus. Because only one driver must be enabled at any time, the output of a disabled or powered down driver is three-stated to avoid overloading the bus.

The receiver inputs have a true fail-safe feature, which ensures a logic high output level when the inputs are open or shorted.

This guarantees that the receiver outputs are in a known state before communication begins and when communication ends. The driver outputs are slew rate limited to reduce EMI and data errors caused by reflections from improperly terminated buses. Excessive power dissipation caused by bus contention or by output shorting is prevented with a thermal shutdown circuit.

The devices are fully specified over the commercial and industrial temperature ranges and are available in 8-lead SOIC (ADM4850 through ADM4857), 8-lead LFCSP (ADM4850 / ADM4852 / ADM4853), and 8-lead MSOP (ADM4850 only) packages.

Features

Electronics industries alliance (EIA) RS-485/RS-422 compliant

Data rate: 500 kbps

Half-duplex and full duplex options

Reduced slew rates for low electromagnetic interference (EMI)

True fail-safe receiver inputs

5 μ A (maximum) supply current in shutdown mode

Up to 256 transceivers on one bus

Outputs high-Z when disabled or powered off

Thermal shutdown and short-circuit protection

Pin-compatible with the MAX308x

Specified over the -40°C to $+85^{\circ}\text{C}$ temperature range

Available in 8-lead SOIC, 8-lead LFCSP, and 8-lead MSOP

Qualified for automotive applications

Application

Low power RS-485 applications

EMI sensitive systems

DTE to DCE interfaces

Industrial control

Packet switching

Local area networks

Level translators

Related Products



[ADV7181CBSTZ](#)

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LQFP-64



[AD724JR](#)

Analog Devices, Inc
SOIC-16



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Analog Devices, Inc
LFSCP-3



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