

HVPAK™ Programmable Mixed-Signal Matrix with Four Outputs with Operating Voltage up to 13.2 V and up to 2 A Current per Output

Manufacturers

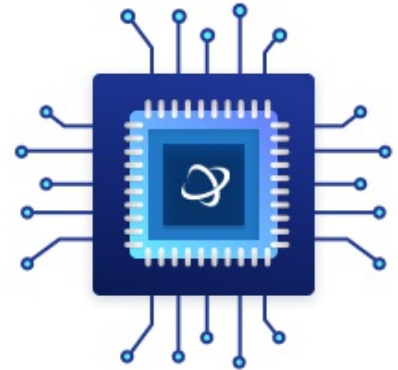
[Renesas Technology Corp](#)

Package/Case

Product Type

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for SLG47105V or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com). We will contact you in 12 hours.

[RFQ](#)

General Description

Features

Up to 13.2V High Voltage and up to 2A High Current Drive GPOs

Full H-Bridge and Independent Half-Bridge Control.

Flexible 8-bit PWMs Macrocells

Constant Current Regulation

Constant Voltage Regulation

Two Wide Range Power Supply Inputs:

2.5 V ($\pm 8\%$) to 5.0 V ($\pm 10\%$) VDD

3.3 V ($\pm 10\%$) to 12.0 V ($\pm 10\%$) VDD2

Built-in Protections:

Undervoltage Lockout

Overcurrent Protection

Thermal Shutdown

I²C control

Operating Temperature Range: -40 °C to 85 °C

One Time Programmable Non-Volatile Memory

Low current consumption (nA level)

RoHS Compliant/Halogen-Free

Macrocells Overview

Four High Voltage, Current Drive GPOs with low RDS_ON

Two 8-bit PWM Macrocells with build-in 16-byte register file

Two Oscillators:

2.048 kHz oscillator

25 MHz oscillator

Two High Speed General Purpose Rail-to-Rail Analog Comparators with Integrated Voltage Reference

Differential Amplifier with Integrator and Analog Comparator

Two Current Sense Comparators with Integrated Voltage Reference

Twelve Combination Function Macrocells:

Three Selectable DFF/Latch or 2-bit LUTs;

One Selectable Programmable Pattern Generator or 2-bit LUT;

Six Selectable DFF/Latch or 3-bit LUTs;

One Selectable Pipe Delay or Ripple Counter or 3-bit LUT;

One Selectable DFF/Latch or 4-bit LUT

I²C Serial Communication Macrocell

Temperature Sensor

Filter with Edge Detector

Programmable Delay with Edge Detector

Power On Reset (POR)

Related Products



[ISL95855HRTZ-T](#)

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48pin-TQFN



[ISL69127IRAZ-T](#)

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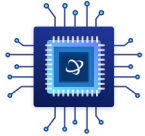
[ISL99380FRZ-TR5935](#)

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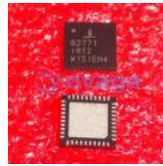
[ISL69125IRAZ-T](#)

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48-QFN