

FPGA Spartan®-II Family 200K Gates 5292 Cells 263MHz 0.18um Technology 2.5V

Manufacturers	AMD Xilinx, Inc
Package/Case	BGA456
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S200-6FGG456C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

XC2S200-6FGG456C is a field-programmable gate array (FPGA) chip produced by Xilinx, a leading manufacturer of programmable logic devices.

Features

It has 200,000 system gates with 2,624 logic cells, 2.1 Mb of internal RAM, and 360 dedicated multipliers.

The device operates at a maximum clock frequency of 175 MHz and has a total of 472 user I/O pins.

It is built using a 0.18 μm CMOS process and is housed in a 456-pin Fine-Pitch Ball Grid Array (FBGA) package.

Application

The XC2S200-6FGG456C is suitable for a wide range of applications, including industrial control, automotive, aerospace, defense, telecommunications, and consumer electronics.

It can be used to implement digital signal processing (DSP), image processing, encryption/decryption, network processing, and other complex functions.

Its high-speed and high-density capabilities make it ideal for applications that require high performance and low power consumption.



Related Products



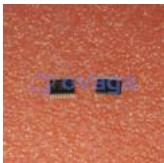
[XC18V01S020C](#)

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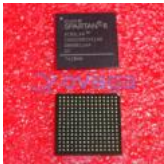
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