

AD5318ARUZ

Data Sheet

2.5 V to 5.5 V Octal Voltage Output 10-Bit DACs in 16-Lead TSSOP; Package: TSSOP; No of Pins: 16; Temperature Range: Commercial

Manufacturers

Analog Devices, Inc

Package/Case

TSSOP16

Product Type

Data Conversion ICs

RoHS

Rohs

Lifecycle



Images are for reference only

Please submit RFQ for AD5318ARUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

The references for the eight DACs are derived from two reference pins (one per DAC quad). These reference inputs can be configured as buffered, unbuffered, or VDD inputs. The parts incorporate a power-on reset circuit, which ensures that the DAC outputs power up to 0 V and remain there until a valid write to the device takes place. The outputs of all DACs may be updated simultaneously using the asynchronous LDAC input. The parts contain a power-down feature that reduces the current consumption of the devices to 400 nA at 5 V (120 nA at 3 V). The eight channels of the DAC may be powered down individually.

All three parts are offered in the same pinout, which allows users to select the resolution appropriate for their application without redesigning their circuit board.

Features

AD5308: 8 buffered 8-bit DACs in 16-lead TSSOP A version: ± 1 LSB INL, B version: ± 0.75 LSB INL

Portable battery-powered instruments

Digital gain and offset adjustment

AD5318: 8 buffered 10-bit DACs in 16-lead TSSOP A version: ±4 LSB INL, B version: ±3 LSB INL

Programmable voltage and current

sources

AD5328: 8 buffered 12-bit DACs in 16-lead TSSOP A version: ±16 LSB INL, B version: ±12 LSB INL

Optical networking

Application

Low power operation: 0.7 mA @ 3 V

Automatic test equipment

Power-down to 120 nA @ 3 V, 400 nA @ 5 V

Mobile communications

Double-buffered input logic

Programmable attenuators

Guaranteed monotonic by design over all codes

Industrial process control

Buffered/unbuffered/VDD reference input options

Output range: 0 V to VREF or 0 V to 2 VREF

Power-on reset to 0 V

ProgrammabilityIndividual channel power-downSimultaneous update of outputs (LDAC)

Low power, SPI- $^{\mathbb{R}}$, QSPI- $^{\mathrm{TM}}$, MICROWIRE- $^{\mathrm{TM}}$, and DSP-compatible 3-wire serial interface





Related Products



ADAS3022BCPZ
Analog Devices, Inc
LFCSP-40



AD574AJNZ
Analog Devices, Inc
PDIP-28



AD7266BSUZ Analog Devices, Inc TQPF-32



AD7401YRWZ
Analog Devices, Inc
SOIC-16



AD7938BSUZ
Analog Devices, Inc
TQFP-32



Analog Devices, Inc TSSOP-24

AD7192BRUZ-REEL



AD7124-8BCPZ-RL7
Analog Devices, Inc
LFCSP-32



AD9680BCPZ-500
Analog Devices, Inc
LFCSP-64