AD7837ARZ-REEL

Data Sheet

LC2MOS Complete, Dual 12-Bit MDAC, (8 + 4) Loading Structure; Package: SOIC - Wide; No of Pins: 24; Temperature Range: Industrial

Images are for reference only

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-24
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for AD7837ARZ-REEL or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

Both parts are microprocessor compatible, with high speed data latches and interface logic. The AD7847 accepts 12-bit parallel data which is loaded into the respective DAC latch using the WR input and a separate Chip Select input for each DAC. The AD7837 has a double-buffered 8-bit bus interface structure with data loaded to the respective input latch in two write operations. An asynchronous LDAC signal on the AD7837 updates the DAC latches and analog outputs.

The output amplifiers are capable of developing ±10 V across a 2 kOhm load. They are internally compensated with low input offset voltage due to laser trimming at wafer level.

The amplifier feedback resistors are internally connected to VOUT on the AD7847.

The AD7837/AD7847 is fabricated in Linear Compatible CMOS (LC2MOS), an advanced, mixed technology process that combines precision bipolar circuits with low power CMOS logic.

A novel low leakage configuration (U.S. Patent No. 4,590,456) ensures low offset errors over the specified temperature range.

Features

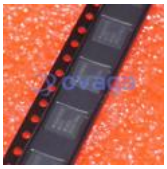
Two 12-Bit MDACs with Output Amplifiers

Space-Saving 0.3", 24-Lead DIP an 24-Terminal SOIC Package

4-Quadrant Multiplication

Parallel Loading Structure: AD7847

Related Products



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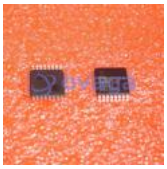
Analog Devices, Inc

LFCSP-40



[AD7266BSUZ]

Analog Devices, Inc

TQPF-32

AD574AJNZ

Analog Devices, Inc

PDIP-28

AD7401YRWZ

Analog Devices, Inc

SOIC-16

AD7938BSUZ

Analog Devices, Inc

TQFP-32

AD7192BRUZ-REEL

Analog Devices, Inc

TSSOP-24

AD7124-8BCPZ-RL7

Analog Devices, Inc

LFCSP-32

AD9680BCPZ-500

Analog Devices, Inc

LFCSP-64