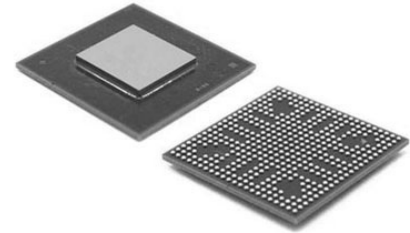


-28 V, -200 mA, Low Noise, Linear Regulator

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	6-Lead LFCSP (2mm x 2mm w/ EP)
Product Type	Power Management ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for ADP7182ACPZN-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADP7182 is a CMOS, low dropout (LDO) linear regulator that operates from -2.7 V to -28 V and provides up to -200 mA of output current. This high input voltage LDO is ideal for regulation of high performance analog and mixed signal circuits operating from -27 V down to -1.2 V rails. Using an advanced proprietary architecture, it provides high power supply rejection and low noise, and achieves excellent line and load transient response with a small 2.2  $\mu$ F ceramic output capacitor.

The ADP7182 is available in fixed output voltage and an adjustable version that allows the output voltage to range from -1.22 V to -VIN + VDO via an external feedback divider.

The following fixed output voltages are available from stock: -5 V (3 mm x 3 mm LFCSP), -1.8 V, -2.5 V, -3 V, -5 V (TSOT), -1.2 V, -1.5 V, -2.5 V, -5 V (2.00 mm x 2.00 mm LFCSP). Additional voltages are available by special order.

The ADP7182 regulator output noise is 18  $\mu$ V rms independent of the output voltage. The enable logic is capable of interfacing with positive or negative logic levels for maximum flexibility.

The ADP7182 is available in 5-lead TSOT, 6- and 8-lead LFCSP packages for a small, low profile footprint.

## Features

Low noise: 18  $\mu$ V rms

Power supply rejection ratio (PSRR): 66 dB at 10 kHz at >

Positive or negative enable logic

Stable with small 2.2  $\mu$ F ceramic output capacitor

Input voltage range: -2.7 V to -28 V

Maximum output current: -200 mA

Low dropout voltage: -185 mV at -200 mA load

Initial accuracy:  $\pm$ 1%

Accuracy over line, load, and temperature

Low quiescent current, >

Low shutdown current: -2  $\mu$ A

Adjustable output from -1.22 V to -VIN + VDO

Current-limit and thermal overload protection

6- and 8-lead LFCSP and 5-lead TSOT

Supported by

## Application

Regulation to noise sensitive applications

Analog-to-digital converter (ADC) and digital-to-analog converter (DAC) circuits, precision amplifiers

Communications and infrastructure

Medical and healthcare

Industrial and instrumentation

## Related Products



### [ADP3336ARMZ-REEL7](#)

Analog Devices, Inc  
MSOP-8



### [ADP3367ARZ](#)

Analog Devices, Inc  
SOIC-8



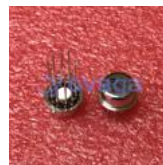
### [ADP3330ARTZ3.3-RL7](#)

Analog Devices, Inc  
SOT-23-6



### [AD737JRZ](#)

Analog Devices, Inc  
SOP-8



### [AD636JH](#)

Analog Devices, Inc  
TO-100-10



### [ADR434BRZ](#)

Analog Devices, Inc  
SOIC-8



[ADR421ARZ](#)

Analog Devices, Inc  
SOP-8



[ADR3412ARJZ-R7](#)

Analog Devices, Inc  
SOT-23-6