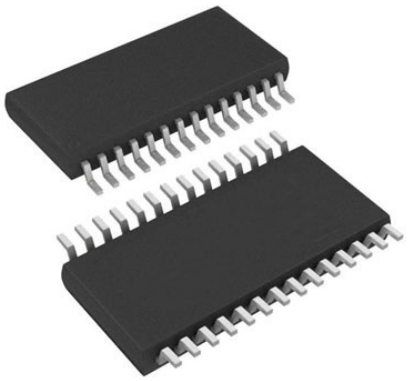
AD7934BRUZ

Data Sheet

Analogue to Digital Converter, Quad, 12 bit, 1.5 MSPS, Differential, Single Ended, Parallel, Single

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-28
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD7934BRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The AD7933/AD7934 feature four analog input channels with a channel sequencer that allows a preprogrammed selection of channels to be sequentially converted. These parts can accept either single-ended, fully differential, or pseudo differential analog inputs.

The conversion process and data acquisition are controlled using standard control inputs that allow for easy interfacing to microprocessors and DSPs. The input signal is sampled on the falling edge of CONVST, and the conversion is also initiated at this point.

The AD7933/AD7934 has an accurate on-chip 2.5 V reference that is used as the reference source for the analog-to-digital conversion. Alternatively, this pin can be overdriven to provide an external reference.

These parts use advanced design techniques to achieve very low power dissipation at high throughput rates. They also feature flexible power management options. An on-chip control register allows the user to set up different operating conditions, including analog input range and configuration, output coding, power management, and channel sequencing.

Product Highlights

High throughput with low power consumption.

Four analog inputs with a channel sequencer.

Accurate on-chip 2.5 V reference.

Single-ended, pseudo differential or fully differential analog inputs that are software selectable.

Single-supply operation with VDRIVE function. The VDRIVE function allows the parallel interface to connect directly to 3 V or 5 V processor systems independent of VDD.

No pipeline delay.

Accurate control of the sampling instant via a CONVST input and once-off conversion control.

Features

Throughput rate: 1.5 MSPS

Specified for VDD of 2.7 V to 5.25 V

Low power 6 mW maximum at 1.5 MSPS with 3 V supplies 13.5 mW maximum at 1.5 MSPS with 5 V supplies

Software configurable analog inputs 4-channel single-ended inputs 2-channel fully differential inputs 2-channel pseudo differential inputs

4 analog input channels with a sequencer

Accurate on-chip 2.5 V reference ±0.2% maximum @ 25°C, 25 ppm/°C maximum (AD7934)

70 dB SINAD at 50 kHz input frequency

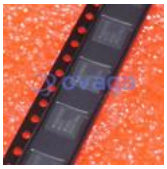
No pipeline delays

High speed parallel interface—word/byte modes

Full shutdown mode: 2 µA maximum

28-lead TSSOP package

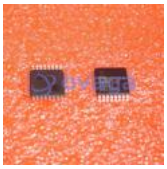
Related Products



ADAS3022BCPZ

Analog Devices, Inc

LFCSP-40



AD7266BSUZ

Analog Devices, Inc

TQPF-32

AD574AJNZ

Analog Devices, Inc

PDIP-28

AD7401YRWZ

Analog Devices, Inc

SOIC-16

AD7938BSUZ

Analog Devices, Inc

TQFP-32

AD7192BRUZ-REEL

Analog Devices, Inc

TSSOP-24

AD7124-8BCPZ-RL7

Analog Devices, Inc

LFCSP-32

AD9680BCPZ-500

Analog Devices, Inc

LFCSP-64