

14-Bit 40/65 MSPS IF Sampling Analog-To-Digital Converter; Package: LQFP; No of Pins: 48; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	LQFP-48
Product Type	Data Conversion ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for AD9244BSTZ-65 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD9244 is a monolithic, single 5 V supply, 14-bit, 65 MSPS analog-to-digital converter with an on-chip, high performance, sample and hold amplifier and voltage reference. The AD9244 uses a multi-stage differential pipelined architecture with output error correction logic to provide 14-bit accuracy at 65 MSPS data rates and guarantees no missing codes over the full operating temperature range.

The AD9244 has an on-board, programmable voltage reference. An external reference can also be chosen to suit the DC accuracy and temperature drift requirements of the application.

A differential clock input is used to control all internal conversion cycles. The digital output data can be presented in straight binary or in twos complement format. An out of range (OTR) signal indicates an overflow condition, which can be used with the most significant bit to determine low or high overflow.

Fabricated on an advanced CMOS process, the AD9244 is available in a 48 pin surface mount plastic package (48-LQFP) and is specified for operation over the industrial temperature range of (-40°C to +85°C).

Features

Low Power:

590 mW at 65 MSPS with F_{in} to Nyquist

340 mW at 40 MSPS with F_{in} to Nyquist

On-Chip Reference and Sample/Hold

750 MHz Analog Input Bandwidth

Guaranteed No Missing Codes over Full Temp Range

1 to 2 V_{pp} Differential Analog Input Range

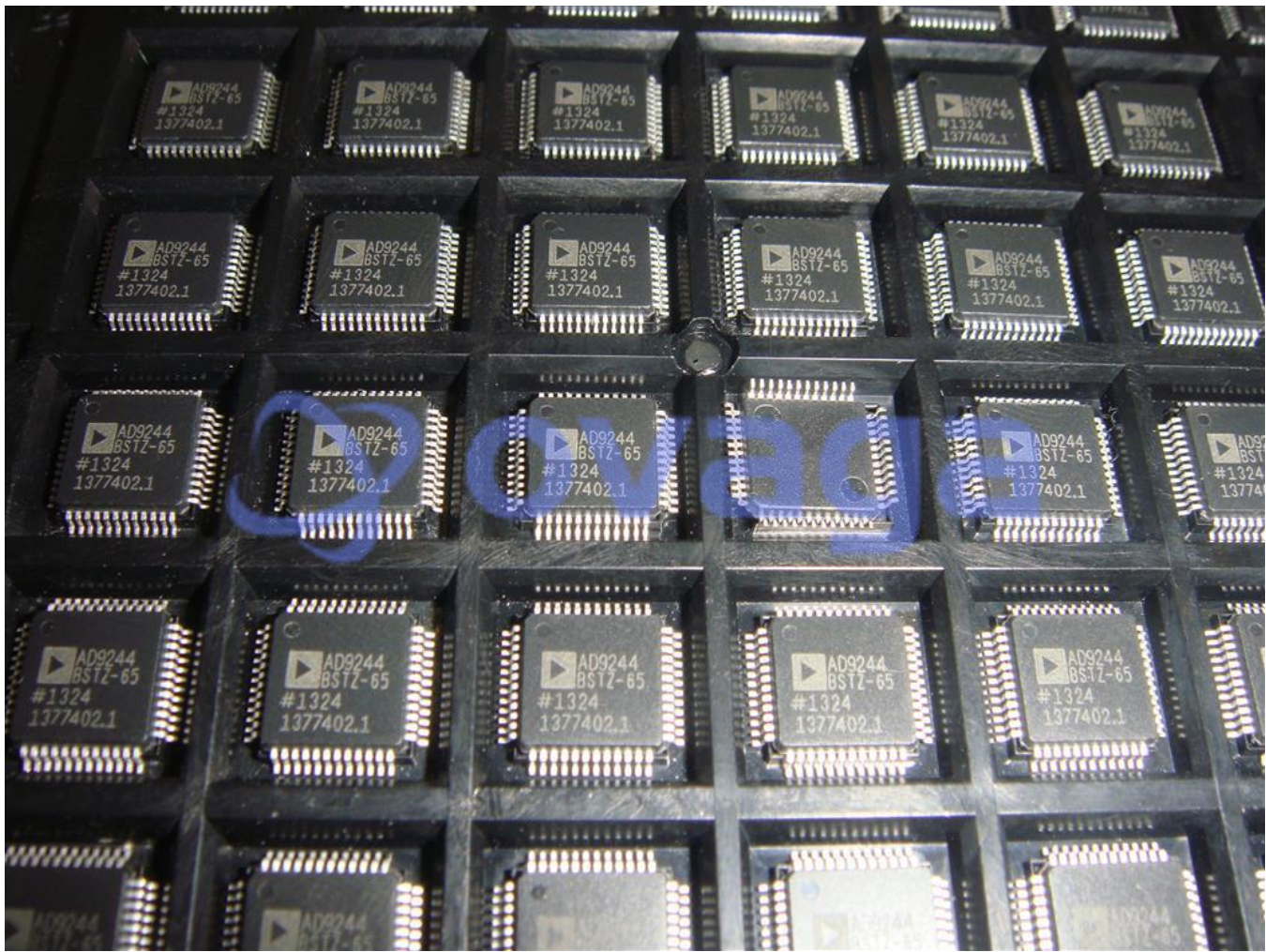
Single +5.0 V Analog Supply 3/5 V Driver Supply

Out-of-Range Indicator

Binary or Twos Complement Output Data

48-LQFP Package





Related Products



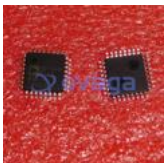
[ADAS3022BCPZ](#)

Analog Devices, Inc
LFCSP-40



[AD574AJNZ](#)

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PDIP-28



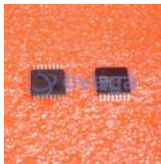
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